

Fig. 1

```

input  P11,P12,P13,P14;
output PO2,PO1;
assign sel1 = P11&P12;
assign sel2= P13|P14;
assign w1 = P12 ^P13;
assign PO2= w2|P11;
always@(sel1,w1,w2)
begin
    case (sel1)
    0 : PO1=w1;
    1 : PO1=w2;
    default : PO1=w1;
    endcase
end
always@(sel2,P11,P13,P14)
begin
    if (sel2)
        w2=P13;
    else
        w2=P14;
    end
end

```

Fig. 2

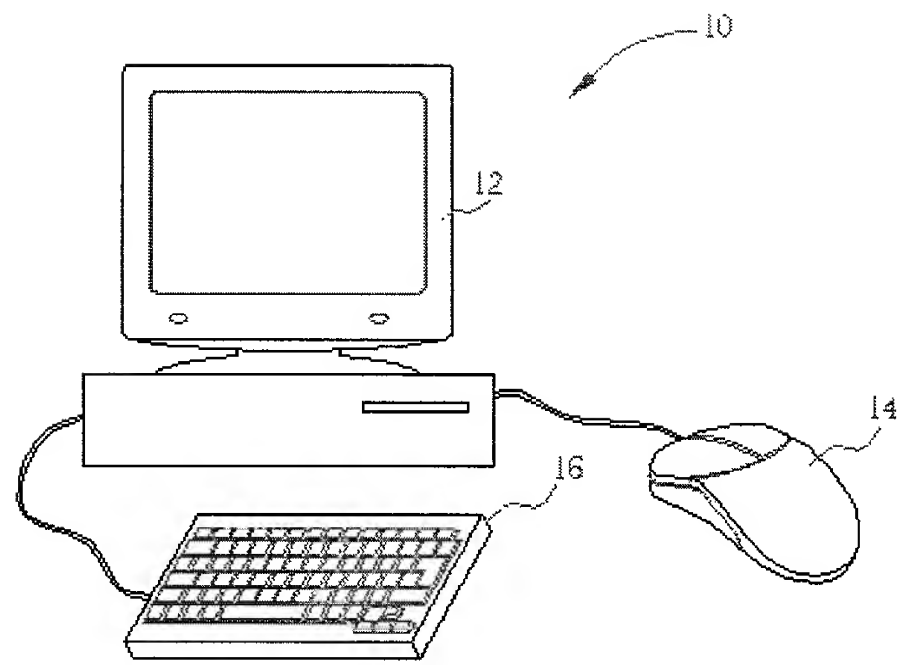


Fig. 3

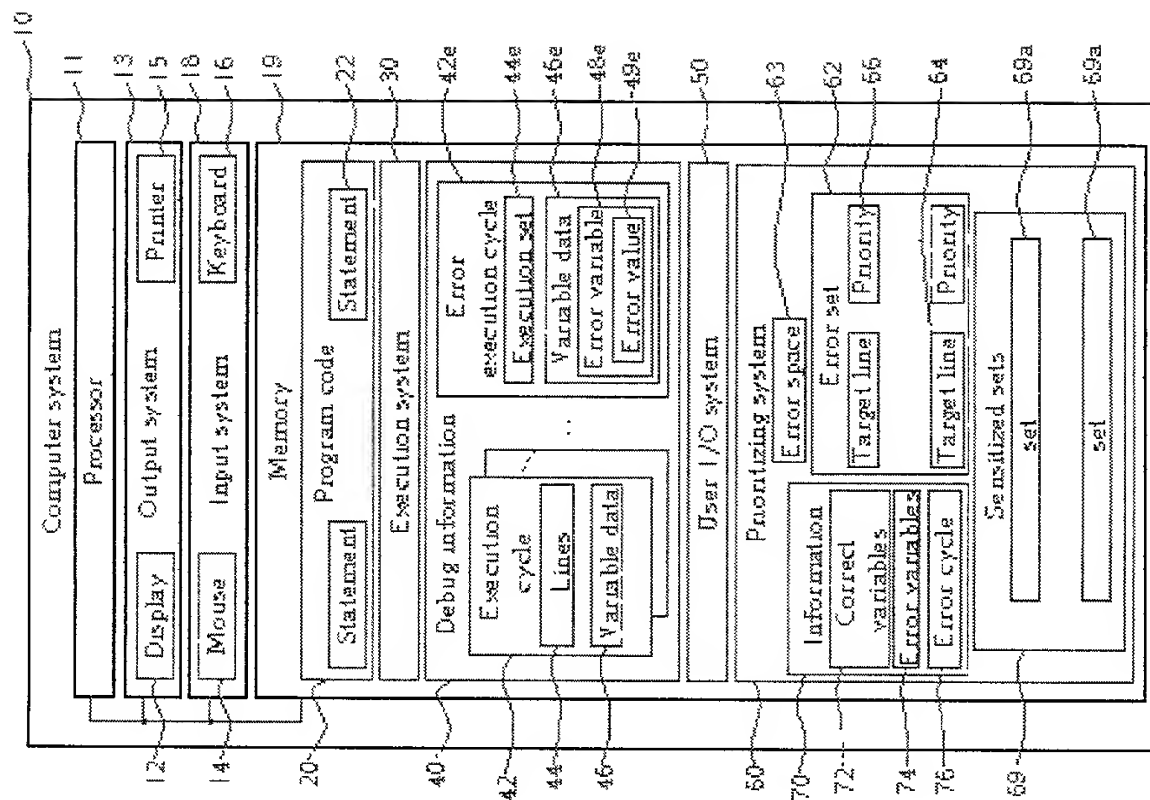


Fig. 4